



Fixed monthly allowance of S\$3,500 100% of MSc in ICD tuition fees1

50% capped at S\$25,000 for the MSc in ICD2

Can be either a new hire or existing employee of the company

Technical University of Munich (TUM) Asia is working closely with the Economic Development Board (EDB) and Singapore Semiconductor Industry Association (SSIA) to provide postgraduate students with industry relevant training in the area of Integrated Circuit Design. With the EDB-IPP Grant, students have the opportunity to take on Research & Development (R&D) roles in the industry while companies are able to attract talent to fulfil their R&D roles. TUM Asia is committed to support training positions which provide postgraduate training to eligible candidates, and to prepare students for research-intensive roles with strong industry relevance and focus.

Trainee Eligibility

- The trainee must be a Singapore Citizen or Permanent Resident.
- The trainee must not be receiving concurrent funding under any EDB grant schemes, or, regardless of the awarding body, any bursaries or scholarships.
- The trainee must meet the prevailing admission criteria for the MSc in Integrated Circuit Design (MSc in ICD).
- There is no bond requirement. The trainee can be either a new hire or existing employee of the company.
- Each trainee will be placed under an ICD training position, in which he/she will be enrolled for the MSc in ICD, while undertaking a research project under their company's supervision for the purposes of completion of the Master's thesis.



Interested applicants please write in to admission@tum-asia.edu.sg

Items which may be claimed under this grant component include: tuition fees, examination registration fees, and fees for other mandatory academic activities. All taxes, including but not limited to GST, are not claimable.

² Items which may be claimed under this grant component must be related to the Project, and include: (i) funds paid to University for usage of lab equipment/consumables at the University R&D laboratory, or software procured for the completion of the academic degree; (ii) funds paid to University to cover overhead charges incurred at the University R&D laboratory; and (iii) funds paid to University and Trainee for publication of research work and participation in research conferences (including the overseas travel expense incurred for such conferences, such as travel insurance, accommodation, meals, local transportation). All taxes, including but not limited to GST, are not claimable.



Trainee Eligibility (continued)

- The training period is up to a cumulative 2 years, commencing when the Trainee becomes an employee of the Company, or when the Trainee is enrolled as a full-time postgraduate student at TUM Asia, whichever is later.
- Training period ceases upon the end of the 2-year support period from the commencement date for the position or upon thesis submission, whichever is earlier.
- As best practice, each Trainee shall spend at least 50% of the training period working on the project at the Company's R&D facilities.

Eligibility Criteria for Companies

- The company must be a Singapore-based entity.
- The company should have a good track record of research activities.
- The company shall appoint, in respect of each Trainee, a non-academic supervisor (known as the Company Supervisor) who has the relevant qualifications and technical competencies.
- The company must demonstrate commitment to support trainee(s) towards attaining the Master in Integrated Circuit Design degree within the stipulated candidature.
- The company will work with TUM Asia to formulate a contractual agreement on intellectual property arrangements and the extent of commitment by all involved parties.
- The company will co-share the research grant to academic faculties for cost incurred at faculties (capped at S\$12,500 per trainee). The company will also fund all other costs related to IPP project and trainee-incurred expenses.
- Interested companies will jointly craft research projects with TUM Asia faculty, before submitting the research proposal to EDB.

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